Introduction to LT-Spice

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LTspice is based on standard software created to simulate electronic circuits called SPICE (Simulated program with Integrated Circuit Emphasis).

Spice was conceived in the 70s at the University of California (Berkeley) and has been improved over the years.

LTspice IV is a free high-performance simulator based on SPICE III, which has a schematic module, for editing electronic schematics as well as a visualization module of the results.

LTSpice contains more than 100 models of operational amplifiers, transistor models, logic gates etc ...

It is very useful for making a first draft of an electronic circuit and for understanding its operation.

LTspice is electronic simulation software that anticipates the characteristics and performance of an electronic circuit by assembling virtual components on the screen. From the spice core developed at Berkeley University but not very user-friendly, the company Linar Technology (LT) has developed a more visual version, easier to use, and free.



How does LTspice work?

LTspice produces an input file that contains the circuit description as well as the commands of the desired analysis type. The input file can be of two forms:

- Either we use the schematics schematic editor of LTspice which will automatically create an input file .asc
- Either we directly describe the circuit using lines of code in a text file called Netlist, extension .net



Drawing the circuit:

Adding a GND: This is very important. You cannot do any simulation on the circuit if you don't have a ground. To place a ground, you can

- press the 'g' key, or
- use the ground icon, \checkmark , or
- get it from the 'Edit' menu.

If you aren't sure where to put it, place it near the bottom of the drawing.

Getting the other Parts:

- The next thing that you have to do is get some or all of the parts you need.
- This can be done by
 - clicking on the icon for a specific component (below picture)

	Resistance	Capacitor	Solenoide	Diode
				/
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V+	D1	VR		
VI	1N914		1	
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				Comment

• Left click on the desired symbol of the Schematic Edit bar

- Ctrl-R and Ctrl-M to rotate it or to get its symmetrical (mirror)
- Move the mouse where you want to position the element
- Left click to "fix" the item



dependent Voltage Source - V1						
Functions		DC Value				
(none)		DC value:				
PULSE(V1 V2 Tdelay Trise Tfall Ton Period Noycles) SINE(Voffset Vamp Freq Td Theta Phi Noycles) EXP(V1 V2 Td1 Tau1 Td2 Tau2) SFFM(Voff Vamp Fcar MD1 Fsig)		Make this information visible on schematic: Small signal AC analysis(AC) AC Amplitude:				
				Pw/L(t1 v1 t2 v2)		AC Phase:
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		Parasitic Properties Series Resistance[Ω]:				
		Parallel Capacitance[F]:				
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- Right click on this symbol then click on Advanced:
- The following symbol is obtained in Schematic for a voltage source
- Choice between different types of
- voltage sources:
- pulsed

For example we take a source of sinusoidal voltage with a frequency of 50Hz, voltage Voffset = 3V and Vamp = 10V

Independent Voltage Source - V1	X	Time-Varying Signal	Spice Description	
Independent Voltage Source - VI Functions (none) PULSE(V1 V2 Tdelay Trise Tfall Ton Period Ncycles) SINE(Voffset Vamp Freq Td Theta Phi Ncycles) EXP(V1 V2 Td1 Tau1 Td2 Tau2) SFFM(Voff Vamp Fcar MDI Fsig) PwL(t1 v1 t2 v2) PwL FILE: Browse	DC Value DC value DC value Make this information visible on schematic: Small signal AC analysis(AC) AC Amplitude: AC Phase: Make this information visible on schematic: Plassic Properties Script Residement(D)	$V_{a} + V_{a}$ V_{a} V_{a		
DC offset(V): 3V Amplitude(V): 10V Freq(Hz): 50hd Tdelay(s): Theta(1/s): Phi(deg):	Parallel Capacitance[F] Parallel Capacitance[F] Make this information visible on schematic: 📝	V_2 V_1 U_1 U_1 U_1 U_2 U_1 U_2 U_1 U_2 U_2 U_1 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2 U_2	Vname $n + n - PULSE(V_1 V_2 t_d t_r t_f PW T)$	
Ncycles: Additional PWL Points Make this information visible on schematic: 📝	Cancel OK	$\begin{array}{c} & & & & & & & & & & & & & & & & & & &$	Vname $n + n - PWL(t_1, v_1, t_2, v_2 t_n, v_n)$	

- To create an electrical connection between the different elements, you must (Left):
- *It is imperative to put a reference potential otherwise no simulation can work (Center).*
- <u>Labels are useful if you want to use your own node names (to find more easily a voltage or</u> <u>current in the circuit) instead of the numbering automatically provided by LTspice (Right)</u>
- Select the "Draw Wire" button
- Left click on the starting node
- Left click on the arrival node
- Right click to stop the Draw option
- wire "

- Select the "ground" button
- Left click where you want it
- to place
- We can also use the "g" key as
- shortens Right click to stop the Draw option
- wire "

- To add a "Net label", press F4 or use the icon
- A window will open:
- Write the desired name
- Click OK
- Click once on a circuit wire where
- you want to get this nomenclaturekey as
- shortensRight click to stop the Draw option
- wire "
- Name the positive potential of the source Vin, the reference potential Vref and the voltage of the resistance Vout

	Ground Label			
	Draw wire			
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- <u>*Polarization Analysis:*</u> This is an extremely powerful analysis since it calculates the steady state of the circuit, therefore, the set of voltages and currents through the circuit components.
- <u>Transient Analysis</u>: It calculates all the variables of the circuit as a function of time.
- <u>Analysis Small AC signal</u>: With this analysis, LTSpice automatically calculates the polarization point of the circuit and then establishes the equivalent small-signal scheme of all the non-linear elements of the circuit (diodes, bipolar transistors, etc ...). She allows them to display the amplitude and phase of the different magnitudes of the circuit as a function of the frequency when an infinitesimal amplitude signal is applied to the circuit.
- <u>DC scan (DC sweep)</u>: This analysis makes it possible to visualize the influence of a DC variable on the other parameters of the circuit (currents, voltages, ...)
- <u>Noise</u>
- <u>DC Transfer</u>

The advantages of LTSPICE:

- An unlimited number of nodes
- ➢ A Schematics editor to draw circuits
- > A visualization editor of the results
- > A large library of components
- ➤ Free